PATENT
Atty. Dkt. No. YOR920030469US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:
Cyril Cabral Jr., et al.

Serial No.: 10/669,898

Confirmation No.: 2246

Filed: September 24, 2003

For: METHOD AND APPARATUS FOR FABRICATING CMOS FIELD EFFECT TRANSISTORS

Group Art Unit: 2824

Examiner: Christian D Wilson

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37 CFR 1.8
I hereby certify that this correspondence is being transmitted by facsimile to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on September 1, 2005. Facsimile No. (571) 273-8300.

September 1, 2005
Date

Kristen Hunter
Signature

Dear Sir:

RESPONSE TO FINAL OFFICE ACTION DATED JULY 1, 2005

In response to the Final Office Action dated July 1, 2005, having a shortened statutory period for response set to expire on October 1, 2005 (3 month date), please enter this response and reconsider the claims pending in the application for reasons discussed below.

PAGE 3/9 * RCVD AT 9/1/2005 4:33:21 PM [Eastern Daylight Time] * SVR:USPTO-EFXRF-6/36 * DNIS:2738300 * CSID:732 530 9808 * DURATION (mm-ss):02-10